



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Gyle D. Yearsley et al.  
Title: Context Switching Pipelined Microprocessor  
Application No.: 09/909,507 Filing Date: July 20, 2001  
Examiner: Pan, Daniel H. Group Art Unit: 2183  
Docket No.: ZILG.252US0 Conf. No.: 4144

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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(c)**

09/01/2004 SDENB0B1 00000102 09909507

01 FC:1806

180.00 0P

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

Copies of the documents listed on the accompanying Form PTO-1449 are enclosed.

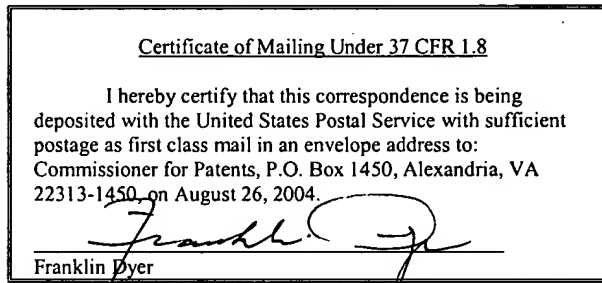
Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(c). A check including \$180.00 for the information disclosure statement fee under 37 C.F.R.

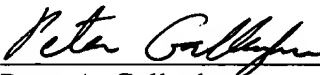
Attorney Docket No.: ZILG.252US0

Application No.: 09/909,507

§ 1.17(p), is enclosed. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.



Respectfully submitted,

  
Peter A. Gallagher  
Reg. No.47,584

8/26/04  
Date

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U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ZILG.252US0	09/909,507
	Applicant(s)	Conf. No.
(Use several sheets if necessary)	Gyle D. Yearsley et al.	4144
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	July 20, 2001	2183

## U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

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## U.S. Published Patent Application Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

## Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass	Yes	No	

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	Zilog Technical Manual, Z80 CPU Central Processing Unit, September 1998, pp. 1-22, 81-104.
	Motorola Inc., MPC860 PowerQUICC User's Manual, MPC860UM/AD, July 1998, Rev. 1, pp. 1-i to 1-iv, 1-1 to 1-10, 4-1 to 4-18, 5-1 to 5-12, 6-1 to 6-24.

Examiner

Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.